Dear Prof. Yaow-Ming Chen,

National Taiwan University, Taiwan

Editor-in-Chief,

IEEE Transactions on Power Electronics

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We would like to submit the enclosed manuscript entitled “*Layout Based Ultra-Fast Short-Circuit Protection Technique for Parallel Connected GaN HEMTs,*” which we wish to be considered for publication in IEEE Transactions on Power Electronics. The novelty of the paper is sensing the induced voltage on the layout for detection of short-circuit fault. The method is implemented on a parallel switch half-bridge configuration. It is experimentally shown that the proposed method is able to detect short-circuit fault within 40 ns and it does not harm the normal switching operation.

The paper is composed according to IEEE Transactions on Power Electronics journal submission format. No conflict of interest exits in the submission of this manuscript, and manuscript is approved by all authors for publication. The paper is an original piece and not under consideration for publication elsewhere.

I appreciate your consideration of our manuscript, and I look forward to receiving comments from the reviewers.

Kind regards,

Dr. Ozan Keysan